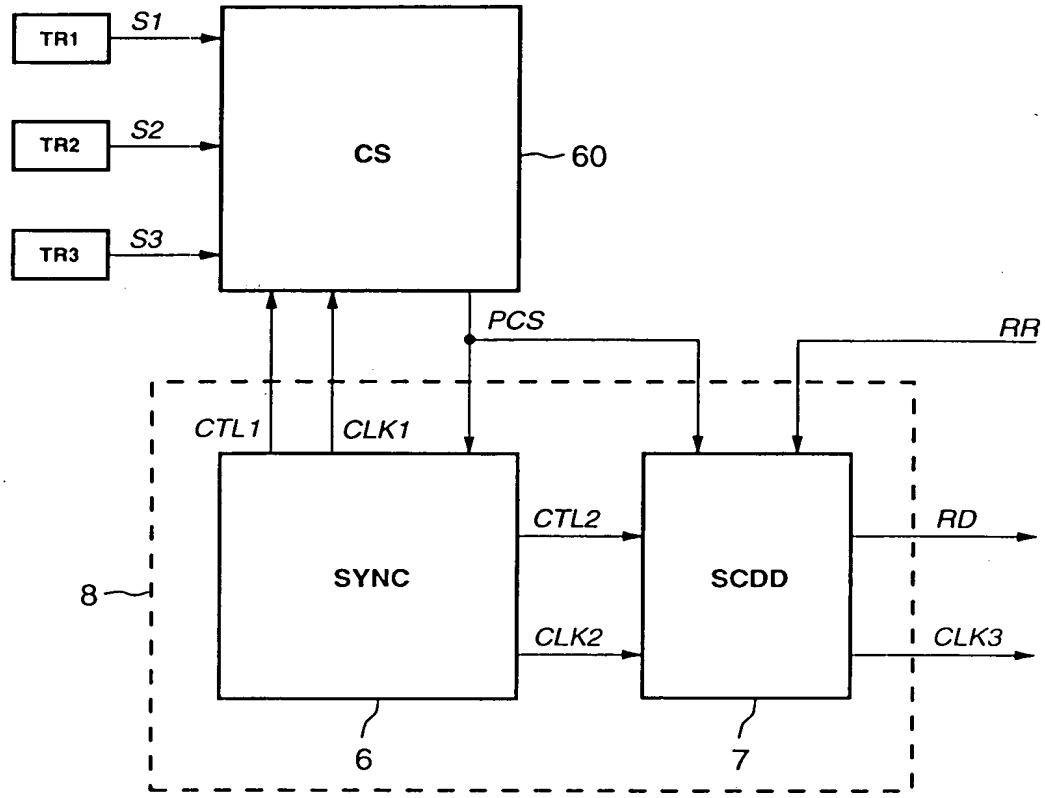
TR1
TR2
TR3
S1
S2
S3
CS
60
PCS
RR
CTL1
CLK1
SYNC
CTL2
CLK2
SCDD
RD
CLK3
8
6
7

Fig. 1

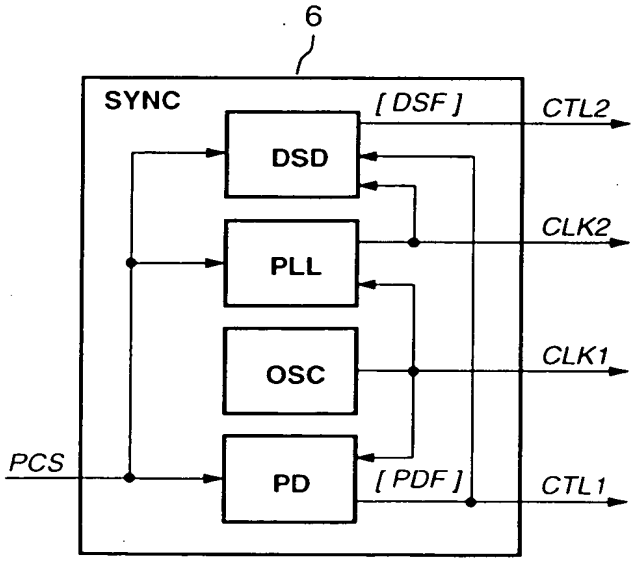
6
SYNC
[DSF]
CTL2
DSD
CLK2
PLL
OSC
CLK1
PCS
PD
[PDF]
CTL1

Fig. 1a

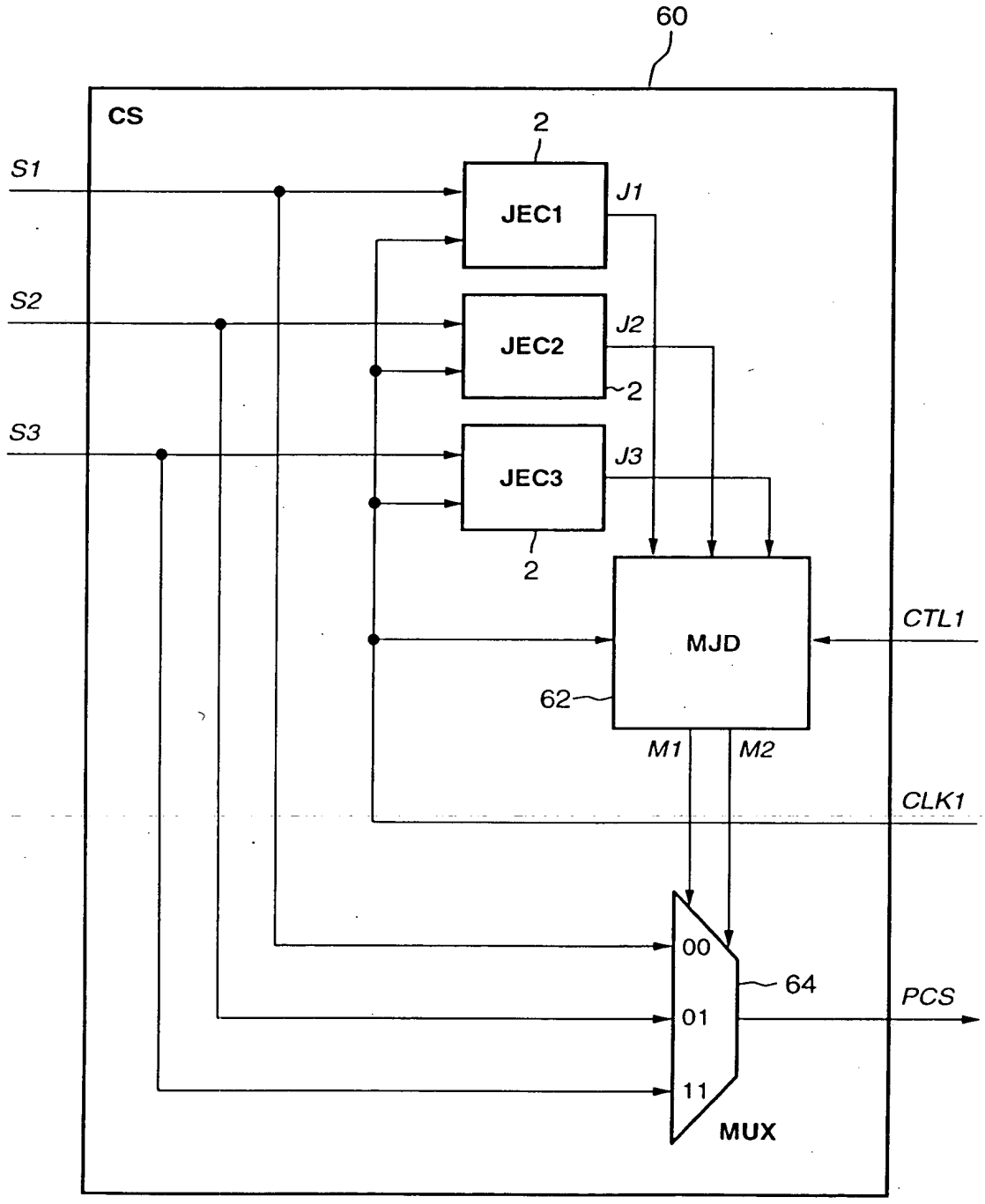
60
CS
2
S1
JEC1
J1
S2
JEC2
J2
2
S3
JEC3
J3
2
MJD
CTL1
62
M1
M2
CLK1
00
64
PCS
01
11
MUX

Fig. 2

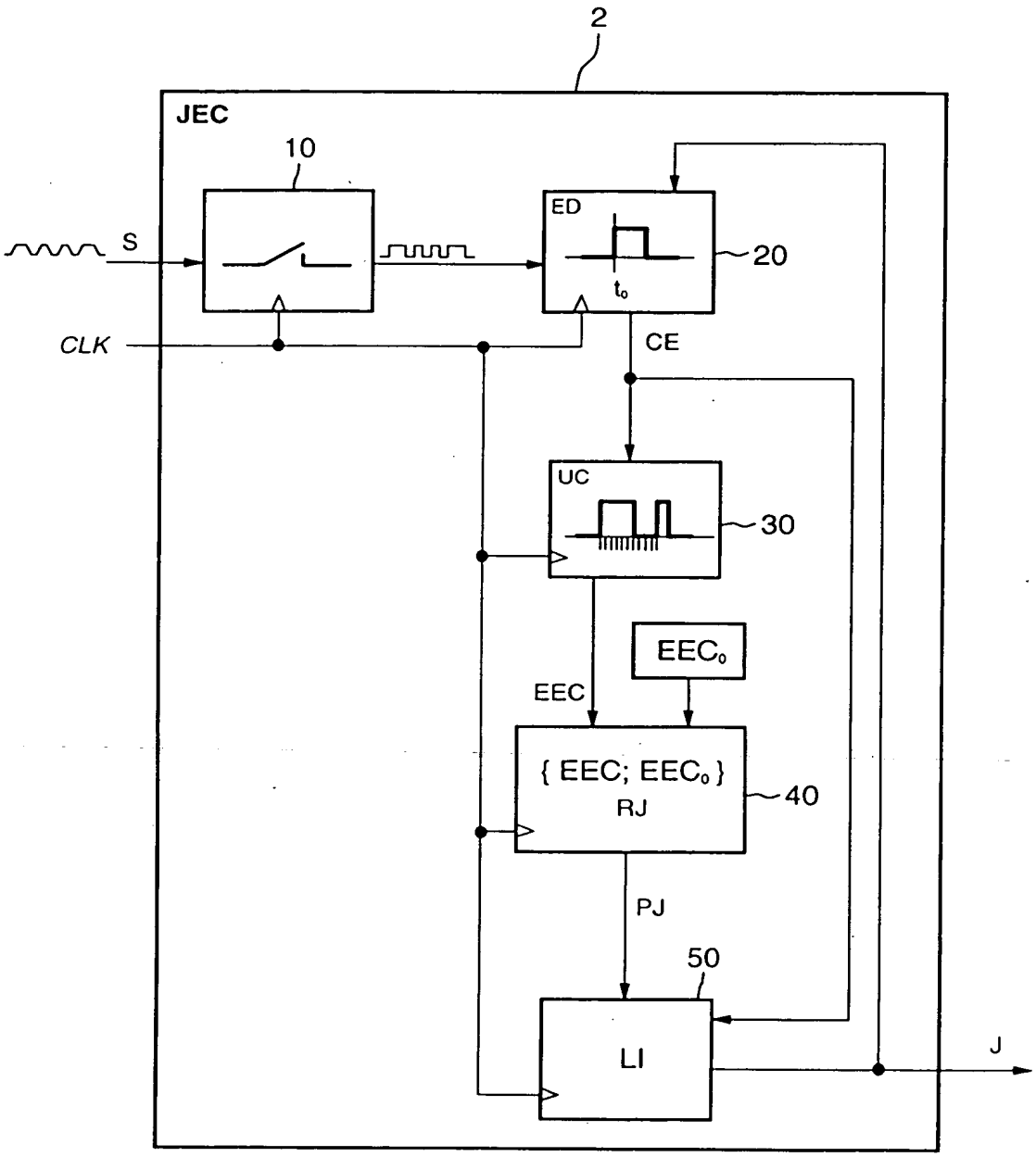

Fig. 3

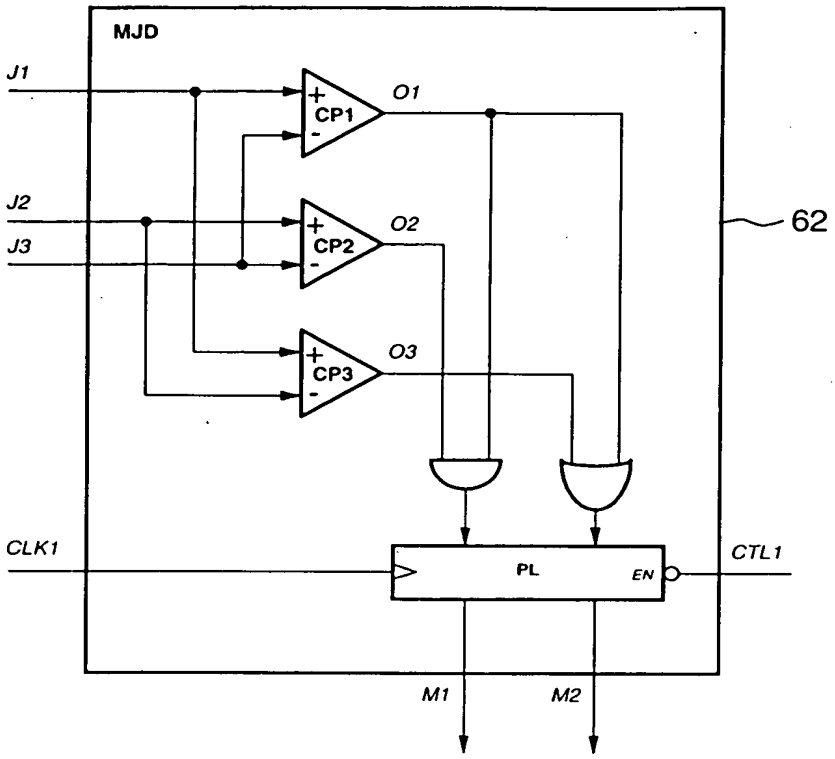
MJD
J1
J2
J3
CP1
CP2
CP3
O1
O2
O3
62
CLK1
PL
EN
CTL1
M1
M2

Fig. 4

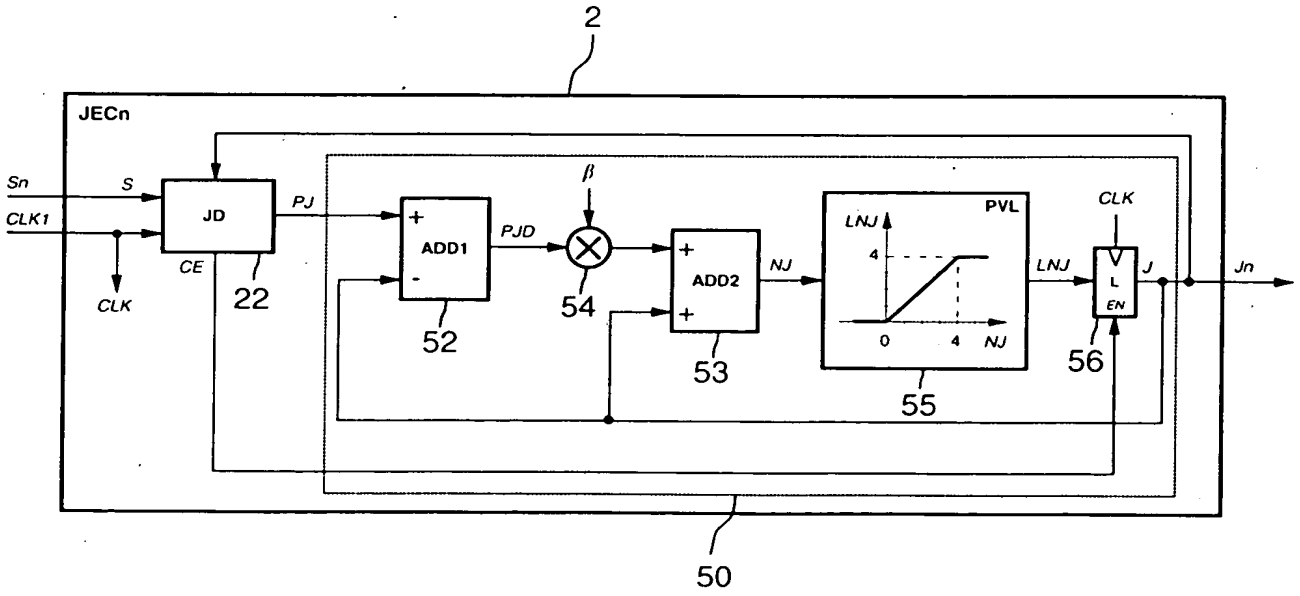
2
JECn
Sn
CLK1
S
JD
CE
CLK
22
PJ
ADD1
52
PJD
β
54
ADD2
53
NJ
LNJ
4
0
4
NJ
PVL
55
CLK
LNJ
L
EN
56
J
Jn
50

Fig. 5

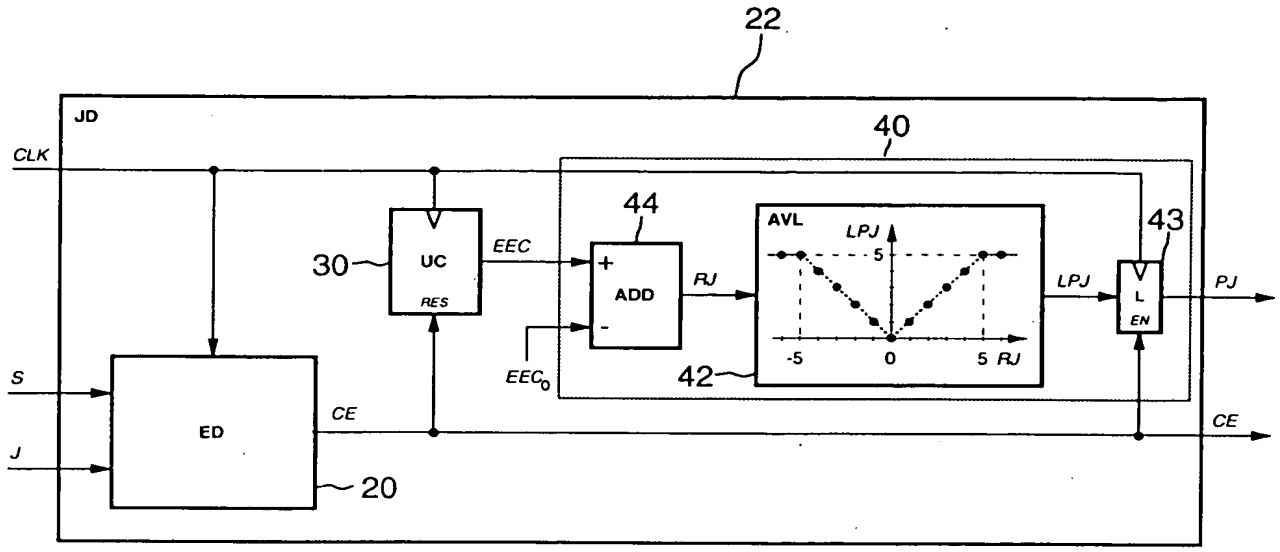
22
JD
40
CLK
44
AVL
LPJ
5
43
30
UC
RES
EEC
ADD
RJ
LPJ
L
EN
PJ
-5
0
5 RJ
EEC0
42
S
ED
CE
CE
J
20

Fig. 6

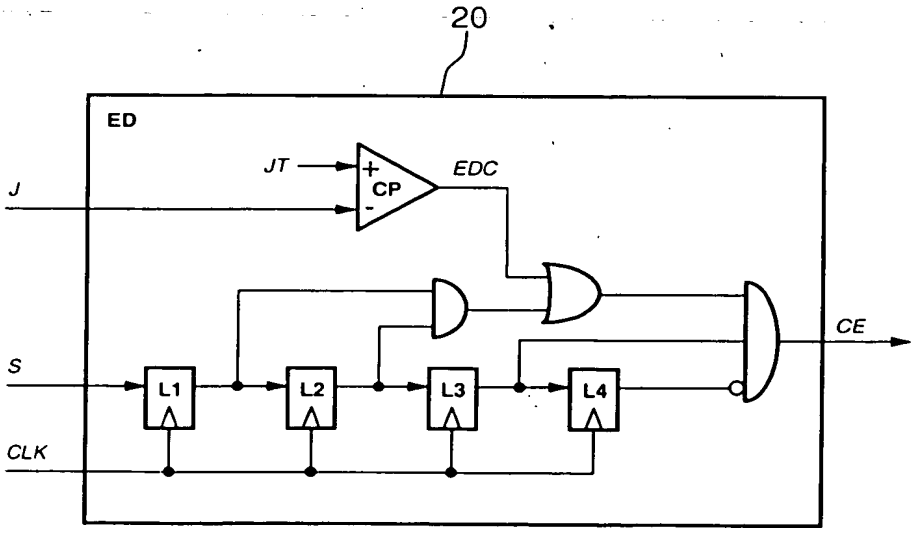
20
ED
JT
CP
EDC
J
CE
S
L1
L2
L3
L4
CLK

Fig. 7

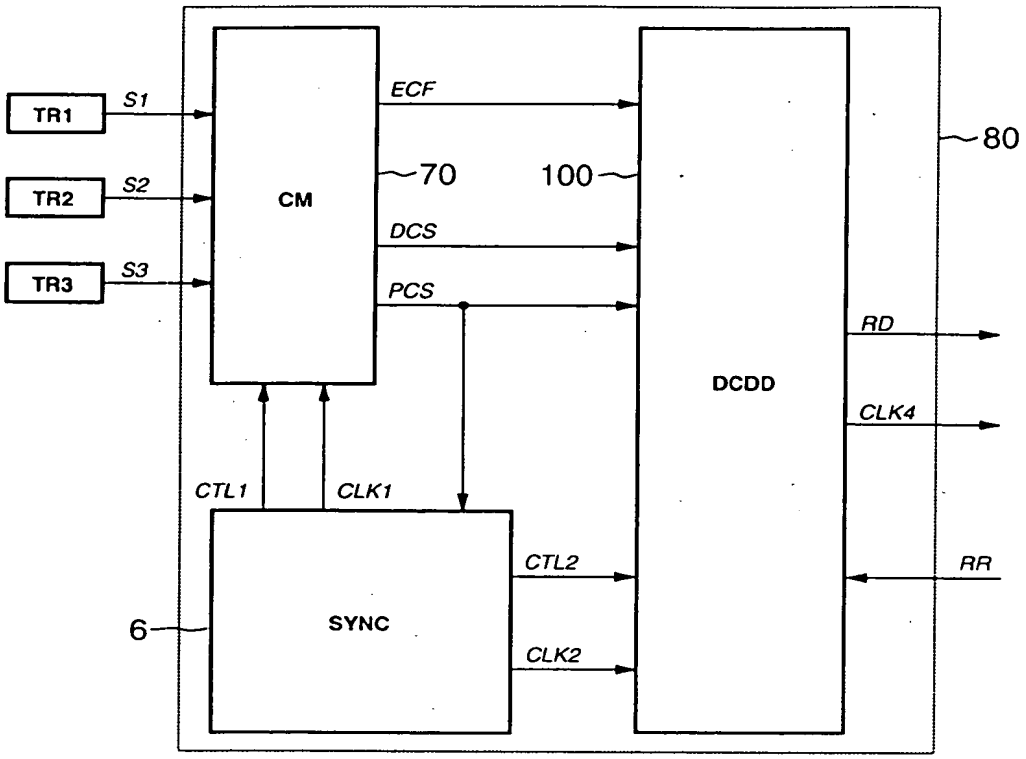
TR1
TR2
TR3
S1
S2
S3
CM
ECF
70
100
DCS
PCS
80
DCDD
RD
CLK4
RR
CTL1
CLK1
SYNC
6
CTL2
CLK2

Fig. 8

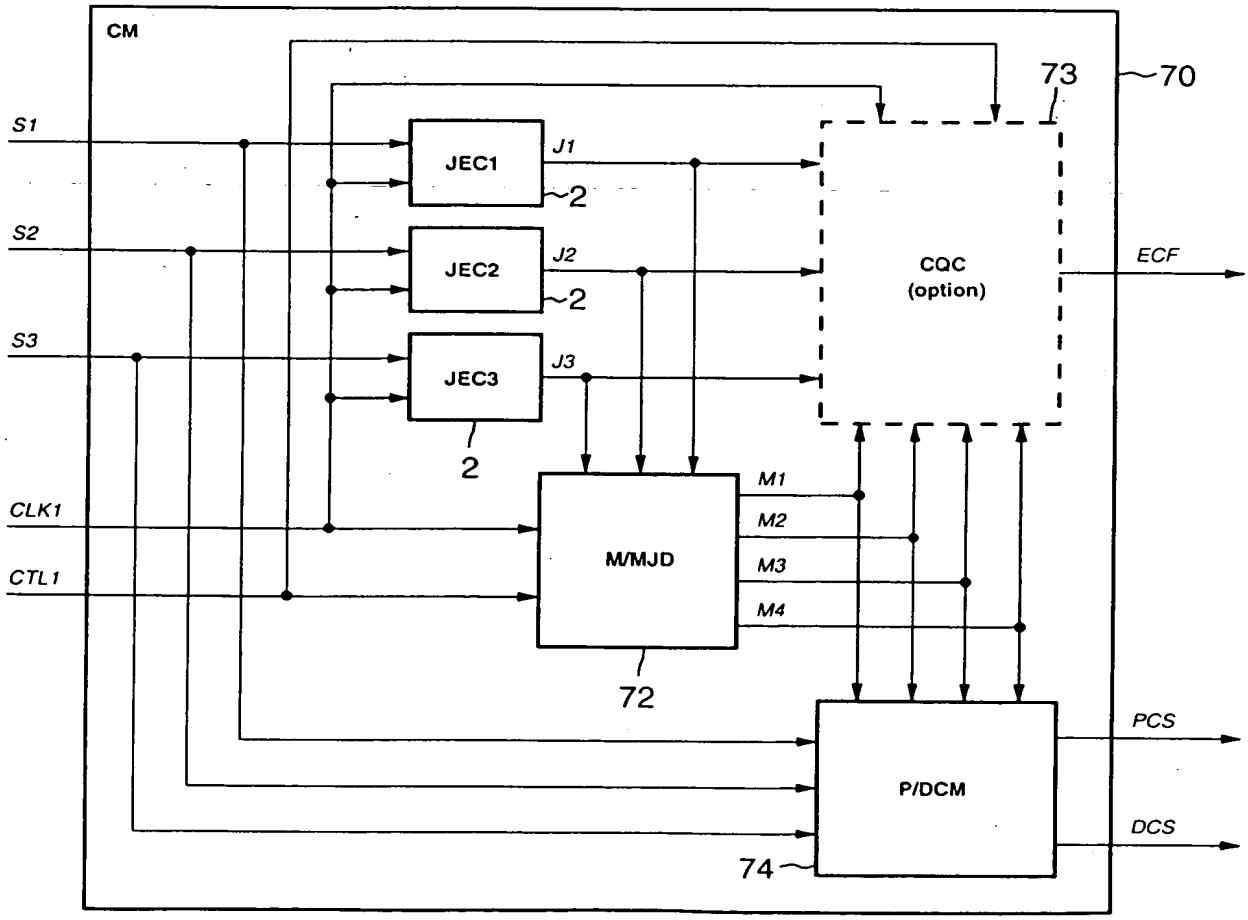
CM
73
70
S1
S2
S3
JEC1
JEC2
JEC3
J1
J2
J3
2
CQC
(option)
ECF
CLK1
CTL1
M/MJD
M1
M2
M3
M4
72
P/DCM
PCS
DCS
74

Fig. 9

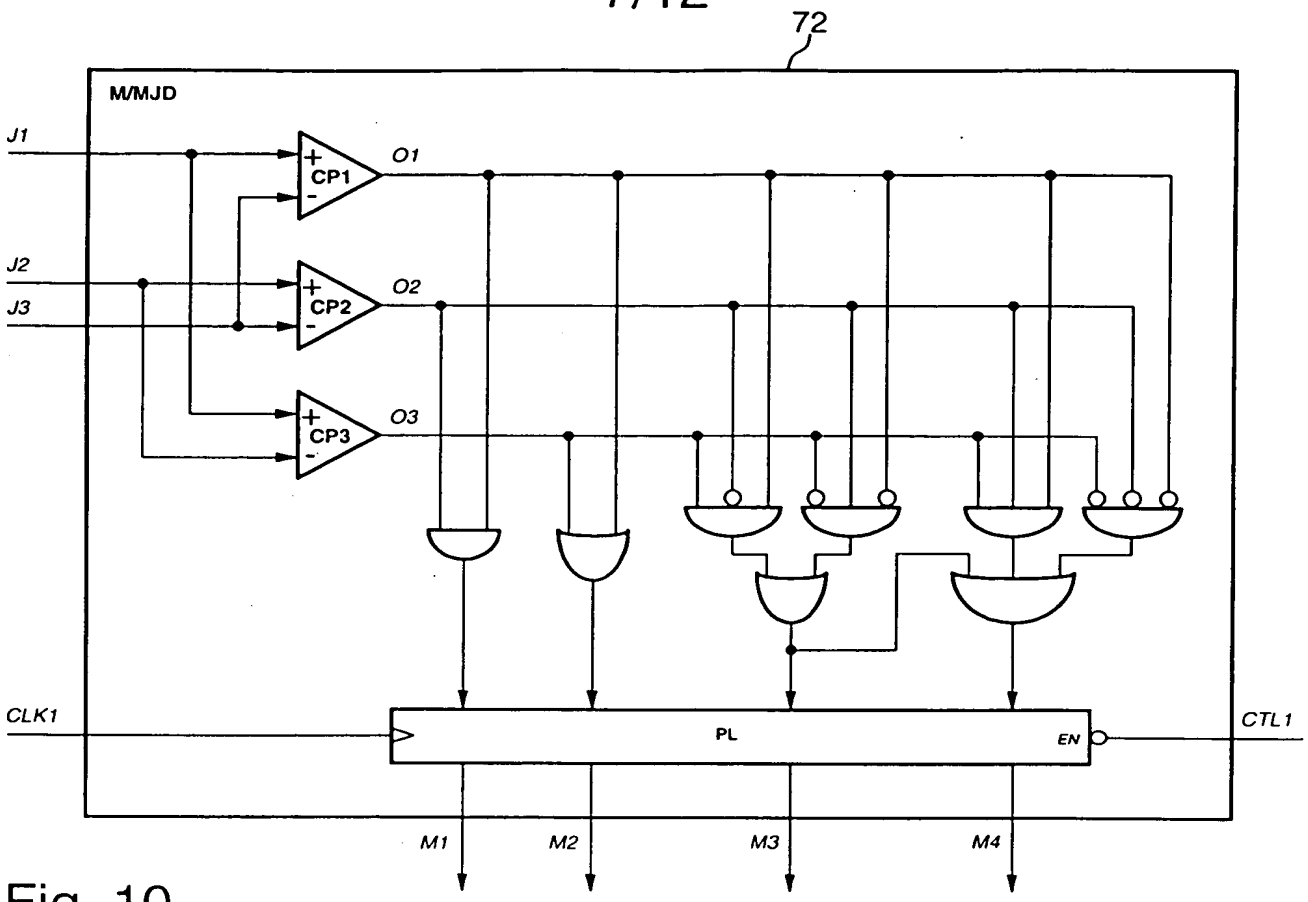

Fig. 10

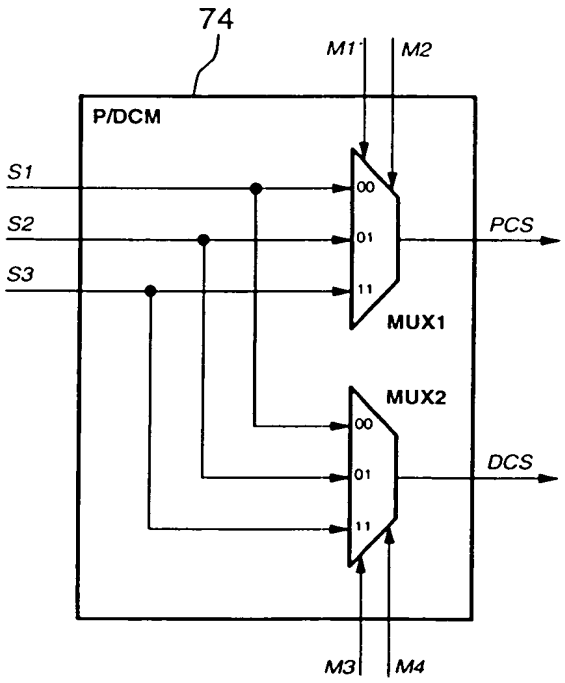

Fig. 11

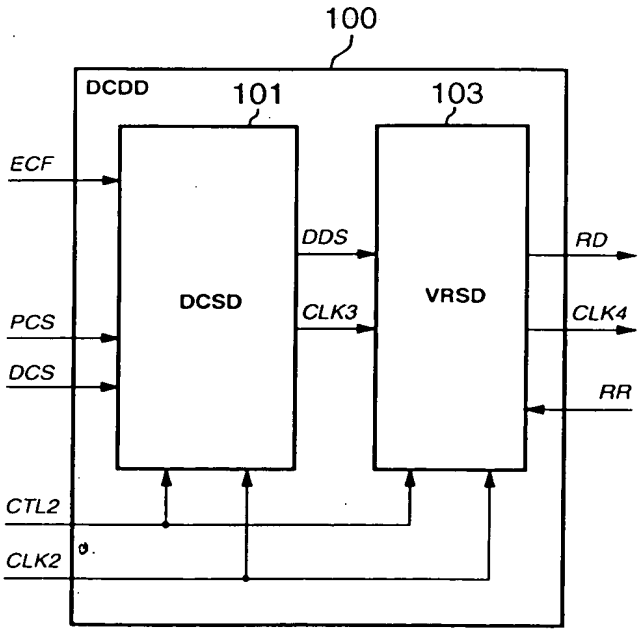

Fig. 12

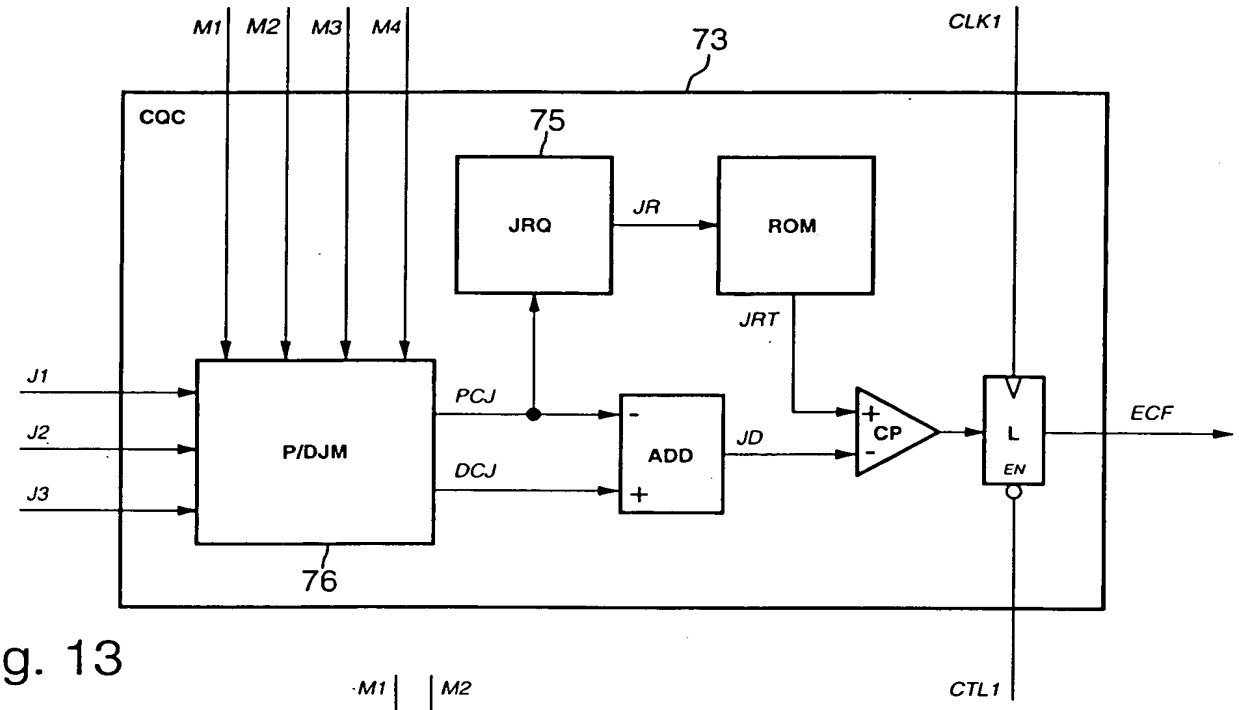

Fig. 13

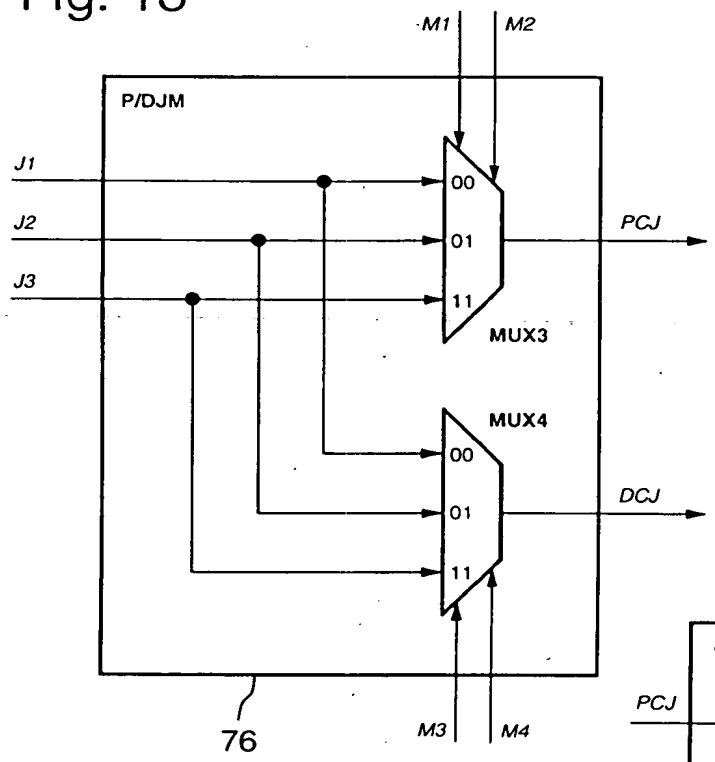

Fig. 14

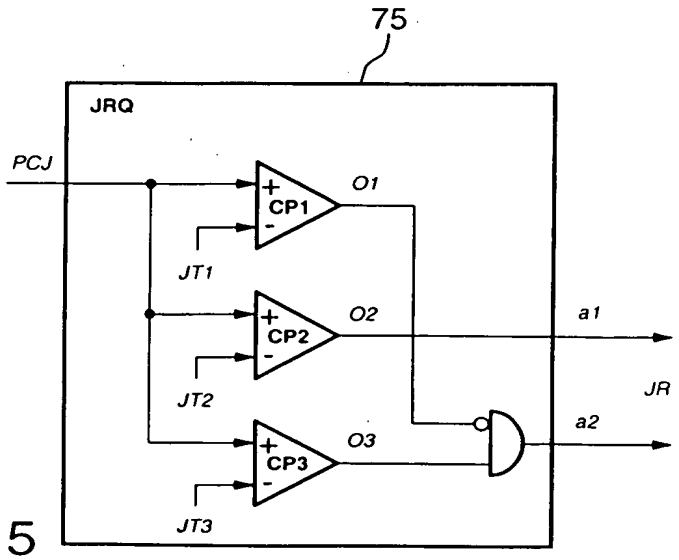

Fig. 15

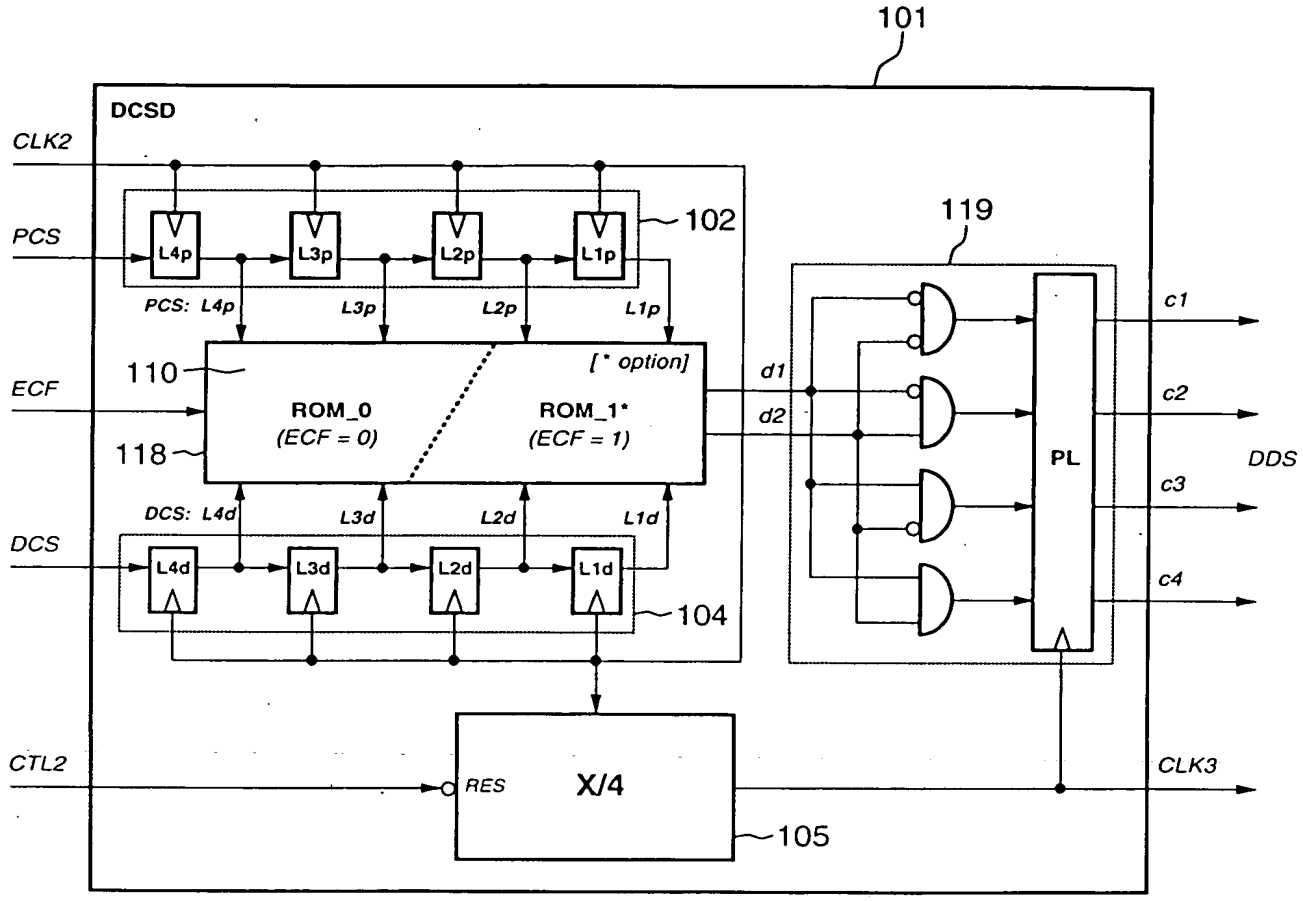
101
DCSD
CLK2
PCS
ECF
DCS
CTL2
L4p
L3p
L2p
L1p
102
PCS: L4p
L3p
L2p
L1p
110
[* option]
ROM_0
(ECF = 0)
ROM_1*
(ECF = 1)
118
DCS: L4d
L3d
L2d
L1d
L4d
L3d
L2d
L1d
104
RES
X/4
105
119
d1
d2
PL
c1
c2
c3
c4
DDS
CLK3

Fig. 16

ROM_0	PCS = {L1p, L2p, L3p, L4p} ⇔ {0, 0, 0, 0} = 0, {0, 0, 0, 1} = 1, ... , {1, 1, 1, 1} = 15															
DCS = {L1d, L2d, L3d, L4d} ⇔ {0, 0, 0, 0} = 0, {0, 0, 0, 1} = 1, ... , {1, 1, 1, 1} = 15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	4	4	3	4	2	2	3	4	1	1	3	3	2	2	1	3
1	4	4	3	4	2	4	2	4	1	4	1	4	1	4	2	4
2	3	4	3	3	2	4	3	3	1	4	3	3	2	2	3	3
3	3	4	3	4	2	4	3	3	1	4	3	4	1	4	3	3
4	2	4	3	3	2	2	2	2	1	1	3	3	2	2	2	2
5	4	4	3	4	2	2	2	4	1	4	1	4	2	2	2	4
6	2	4	3	3	2	2	3	2	1	4	3	3	2	2	3	2
7	4	4	3	4	2	2	3	4	1	4	3	4	2	4	2	4
8	1	4	3	3	2	4	2	3	1	1	1	1	1	1	1	1
9	4	4	3	4	2	4	3	4	1	1	1	4	1	1	1	4
10	1	4	3	3	2	4	3	3	1	1	3	1	1	1	3	1
11	3	4	3	4	2	4	3	3	1	4	1	4	1	4	1	3
12	1	4	3	3	2	2	2	2	1	1	1	1	2	1	2	1
13	2	4	3	4	2	2	2	2	1	1	1	1	1	1	1	2
14	1	4	3	3	2	2	3	3	1	1	3	3	2	2	1	1
15	2	4	3	4	2	2	2	4	1	1	3	3	2	2	1	1

Fig. 17

ROM_1	PCS = {L1p, L2p, L3p, L4p} ⇔ {0, 0, 0, 0} = 0, {0, 0, 0, 1} = 1, ... , {1, 1, 1, 1} = 15															
DCS = {L1d, L2d, L3d, L4d} ⇔ {0, 0, 0, 0} = 0, {0, 0, 0, 1} = 1, ... , {1, 1, 1, 1} = 15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	1	4	3	4	2	4	2	4	1	4	3	4	2	4	3	1
1	4	4	3	4	2	4	3	4	1	4	3	4	2	4	1	4
2	3	3	3	3	3	2	3	3	1	1	3	3	2	2	3	3
3	4	4	3	4	4	4	3	4	1	4	3	4	2	4	3	4
4	2	2	3	4	2	2	2	2	1	1	3	3	2	2	2	2
5	4	4	2	4	2	2	2	4	1	4	3	4	2	2	2	4
6	2	3	3	3	2	2	3	3	2	4	3	3	2	2	2	3
7	4	4	3	4	2	4	3	4	4	4	3	3	2	2	2	4
8	1	1	1	1	1	1	2	4	1	1	1	1	1	1	1	1
9	4	4	1	4	1	4	4	4	1	1	1	1	1	1	1	1
10	3	3	3	3	3	3	3	3	1	1	3	3	1	1	3	1
11	4	4	3	4	3	4	3	3	1	1	3	3	1	4	3	3
12	2	2	2	2	2	2	2	2	1	1	1	1	2	2	1	2
13	4	4	2	4	2	2	2	2	1	1	1	4	2	2	1	2
14	3	1	3	3	2	2	2	2	1	1	3	3	1	1	1	1
15	1	4	3	4	2	4	3	4	1	1	1	3	2	2	1	1

Fig. 18

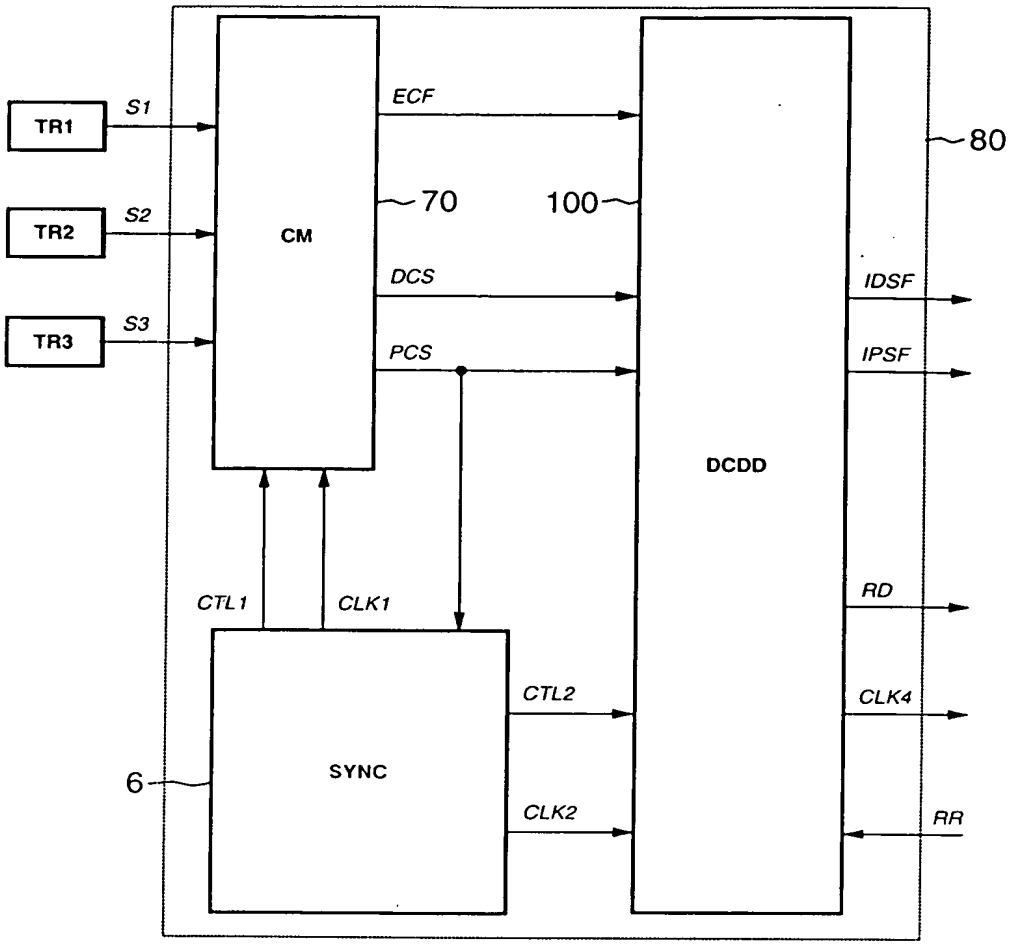
TR1
TR2
TR3
S1
S2
S3
CM
ECF
70
DCS
PCS
100
DCDD
80
IDSF
IPSF
RD
CLK4
RR
CTL1
CLK1
SYNC
6
CTL2
CLK2

Fig. 19

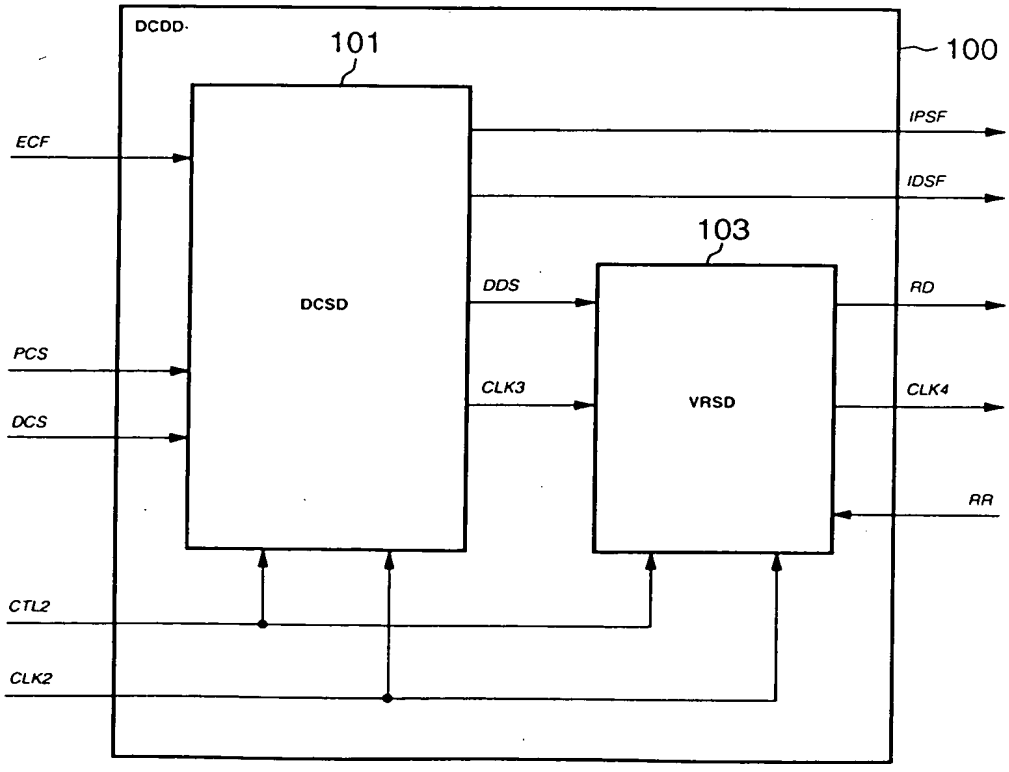
DCDD
101
100
ECF
IPSF
IDSF
103
DCSD
DDS
RD
PCS
CLK3
VRSD
CLK4
DCS
RR
CTL2
CLK2

Fig. 20

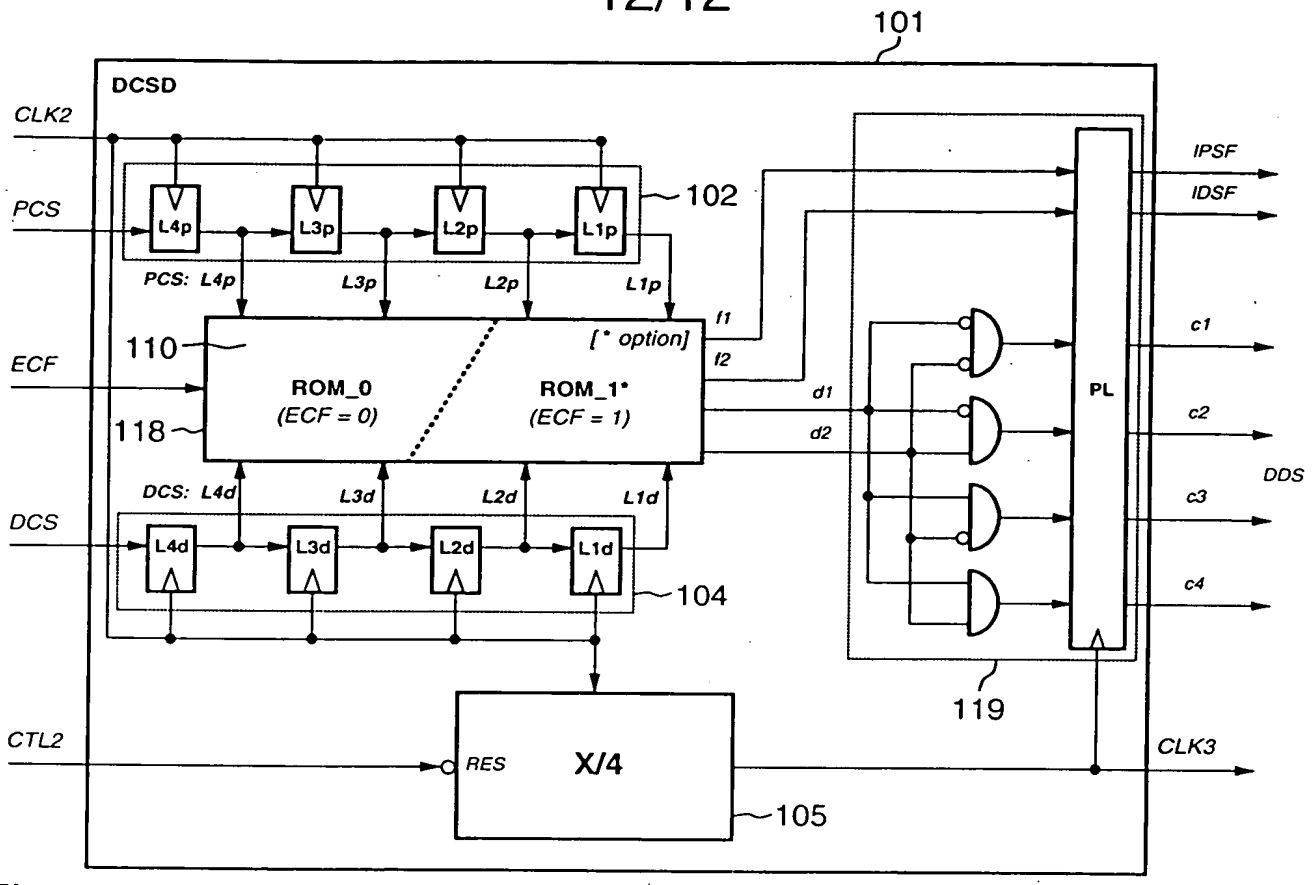
101
DCSD
CLK2
PCS
ECF
DCS
CTL2
L4p
L3p
L2p
L1p
102
PCS: L4p
L3p
L2p
L1p
110
[* option]
ROM_0
(ECF = 0)
ROM_1*
(ECF = 1)
118
f1
f2
d1
d2
DCS: L4d
L3d
L2d
L1d
L4d
L3d
L2d
L1d
104
PL
IPSF
IDSF
c1
c2
c3
c4
DDS
119
RES
X/4
105
CLK3

Fig. 21

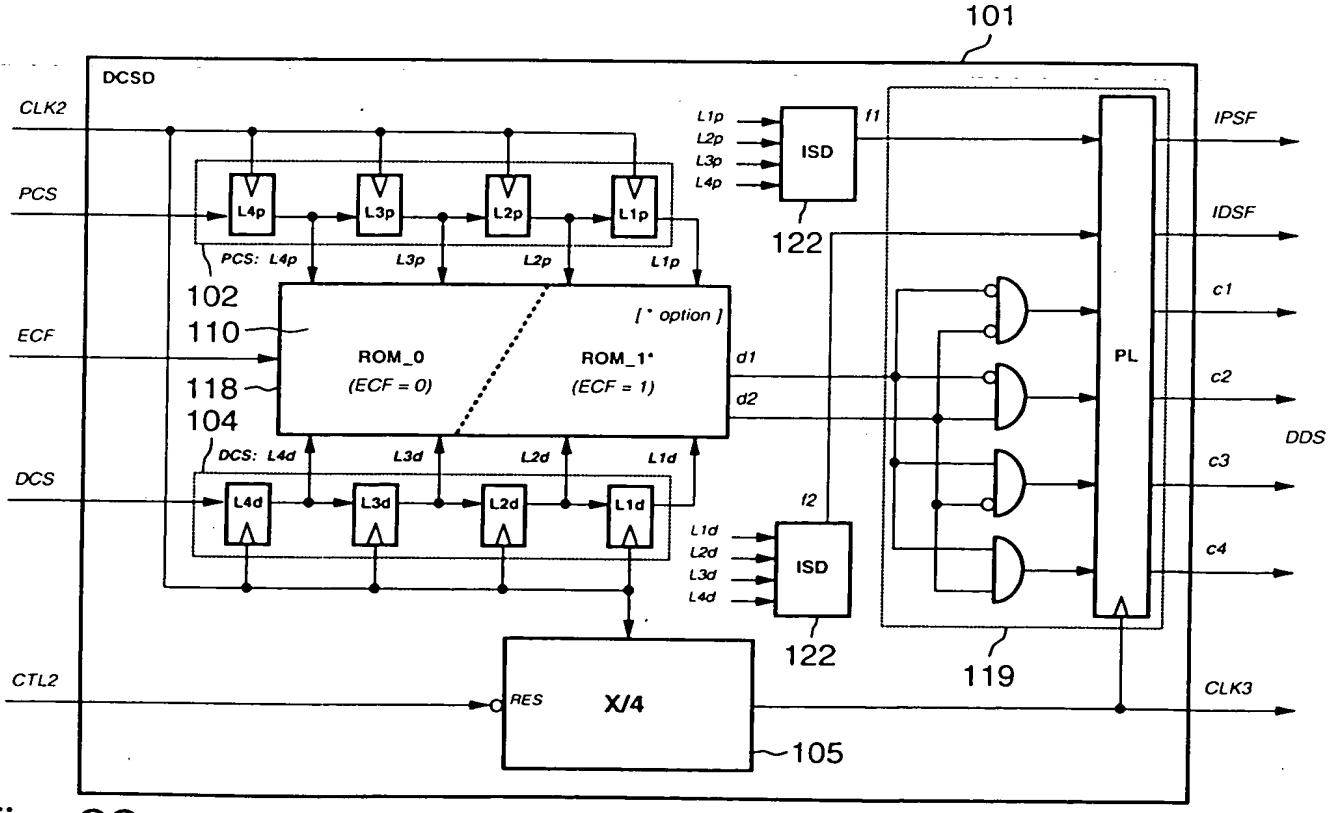
101
DCSD
CLK2
PCS
ECF
DCS
CTL2
L4p
L3p
L2p
L1p
PCS: L4p
L3p
L2p
L1p
102
110
[* option]
ROM_0
(ECF = 0)
ROM_1*
(ECF = 1)
118
104
DCS: L4d
L3d
L2d
L1d
L4d
L3d
L2d
L1d
f1
f2
d1
d2
ISD
122
ISD
122
PL
IPSF
IDSF
c1
c2
c3
c4
DDS
119
RES
X/4
105
CLK3

Fig. 22